

FDP on Advanced VLSI System Design Flow Using Cadence and OrCAD EDA

Semiconductor innovation powers the core of the Electronics System Design and Manufacturing (ESDM) sector, energising technological progress and shaping the future of electronics worldwide.

Join us for an enriching FDP in collaboration with **Uttarakhand Technical University, Dehradun** organised by **Entuple Technologies**.

Registration Link:

<https://online.uktech.ac.in/ums/Student/Public/DevelopmentRegistration>

Date:
01st July to 06th July 2024

Venue:
Pt. Narayan Dutt Tiwari
Conference Hall,
Administrative Building
of Veer Madho Singh
Bhandari Uttarakhand
Technical University,
Dehradun

Who can attend:
Faculty members

Chief Guest:



Dr. Onkar Singh
Vice Chancellor
Uttarakhand Technical
University, Dehradun

Special Guest:



Mr. Mehta S. D.
Founder & Director,
Entuple Technologies,
Bangalore

About UTU:

Established in Dehradun on January 27th, 2005, Uttarakhand Technical University is the state's sole affiliating University for technical institutions. With 81 Affiliated Private Institutions and 12 Government Institutions, it offers 8 UG, 6 PG, and 18 Ph.D. programs across disciplines such as Management, Engineering, and Pharmacy. Additionally, the University has set up 6 Campus Colleges to reach remote areas of Uttarakhand.

About Entuple:

Headquartered in Bangalore, India, Entuple Technologies, founded in 2010, brings together industry professionals with over 80 years of combined experience. Our diverse team pioneers Next Generation Solution Enablers in system design technologies, partnering with industry leaders to provide cutting-edge solutions. We also bridge curriculum-industry gaps in academia by empowering campuses with effective tools and technologies.

Program Outcomes:

1. Gain proficiency in VLSI design principles and methodologies.
2. Learn circuit simulation techniques to predict circuit behavior accurately.
3. Develop competence in utilizing Cadence and OrCAD EDA tools.
4. Gain insights into full custom and semi-custom IC design flows.
5. Acquire expertise in physical design flow using tools like Innovus.
6. Learn RTL synthesis using Genus Synthesis Solution.
7. Acquire skills in post-layout simulation and parasitic extraction techniques.
8. Develop familiarity with PCB design processes and fabrication file generation.
9. Gain a comprehensive understanding of the electronic product design cycle.
10. Understand fundamentals of antenna fabrication, including practical demonstrations.
11. Network with industry experts and peers for collaboration and professional growth.

Special Note:

- Certificates will be provided to all the participants.
- All Hands-on sessions will be provided by industry experts from Entuple Technologies on EDA Tools (Cadence & OrCAD)
- The maximum number of participants will be 30. Merit will be based on a first-come, first-served

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